

John Levy – Summary of technology areas in expert cases

Year	Client law firm or court	Nature of the case	Technology area	Specific areas & activities
2006	Morrison & Foerster, Palo Alto	Patent infringement	Audio/Video storage and playback	Digital video and audio stream storage and retrieval in consumer digital video recorder (DVR)
2005-6	Mintz, Levin, Cohn, Ferris Glovsky & Popeo, Boston	Patent infringement	Bus design for industrial control applications	Prior art search related to single-signal buses
2005-6	Fish & Richardson, Washington, DC	Patent infringement	Multiprocessor cache coherency	Cache coherency in multiple-bus multiprocessor systems with shared memory
2005-6	DLA Piper Rudnick Gray Cary, Palo Alto	Patent infringement	Hard disk design	Hard disk drive commands, ATA standards, creation of a “reserve area” on disk
2005	Meisel & Krentsa, San Francisco	Personal injury	ASIC design engineering	Testimony regarding ASIC design engineer capabilities and activities
2005	Wilson, Sonsini, Goodrich & Rosati, Palo Alto	SEC Wells Notice	Software industry practice	Software product development, customary beta site agreements
2005	Morgan, Lewis & Bockius, Philadelphia	Product liability, potential class action	PC motherboard design	Hardware design, noise & failure analysis; testing of motherboards under heavy input/output load
2005	Cesari & McKenna, Boston	Patent infringement	Graphical programming languages & displays	Industrial application of graphical programming and object-oriented programming languages
2005	Merchant & Gould, Atlanta & Minneapolis	Patent infringement	Computer hardware – bus design	Development of speed improvements for ATA/IDE disk interface, similar to Ultra-ATA standard; analysis of differences between patent and the standard
2005	NautaDutilh NV, Amsterdam	Patent infringement	Computer hardware – memory controller	Memory controller containing “read-around-write” feature, which accelerates performance by executing memory read accesses ahead of queued write accesses; wrote an expert report
2004-2005	Farella Braun & Martel, San Francisco	Breach of contract related to putative chip failures	Hard disk drive	Putative failure of EEPROM chips in hard disk drives due to low resistance between pins; analysis of a database containing failure analysis reports on plaintiff’s disk drives; consultation on discovery and deposition questions; factory visit to disk drive repair facility

2004	U.S. District Court, Western District of Washington, Hon. Marsha Pechman	Patent infringement	Internet (http) protocol	Using “cookies” for load balancing among multiple processors supporting a web server; briefed the Court on technology and terms of art in Internet protocol; attended Markman hearing
2004	Weil, Gotshal & Manges; Dewey Ballantine, New York	Breach of contract and misappropriation of trade secrets	Hard disk drive manufacture	Factory visit to document the manufacturing processes claimed as trade secrets
2003-2004	Squire, Sanders & Dempsey, Palo Alto; Orrick, Herrington & Sutcliffe, Menlo Park	Patent infringement	Computer hardware – memory controller	Memory controller containing “read-around-write” feature, which accelerates performance by executing memory read accesses ahead of queued write accesses; wrote an expert report for three of the defendants; deposition
2003-2004	U.S. District Court, Northern District of California, San Jose, Hon. Ronald Whyte	Misappropriation of software trade secrets	Application software for typeset	Attended a preliminary hearing in order to assist the judge in getting the adversaries and their experts to agree on a format for exchanging source code.
2003-2004	U.S. District Court, Northern District of California, San Jose, Hon. Ronald Whyte	Patent infringement	CD-ROM controller	Briefed the Court on technology and terms of art related to ATA/ATAPI Standard and controller interfaces; consulted on proposed Markman rulings
2001	Kirkland & Ellis, New York	Patent infringement	Computer hardware – memory bus	High-speed memory bus design; researched prior art, preliminary preparation of expert report
2000	Finnegan, Henderson, Farabow, Garrett & Dunner, New York	ITC - patent infringement	Computer hardware – bus design	Bus arbitration methods, including round-robin priority management;
1988	Skjerven, Morrill, McPherson, Franklin & Friel, Santa Clara	Infringement of software copyright	Firmware in a CPU chip	Firmware / microcode which defines the instruction set in a single-chip microprocessor; extensive pre-trial preparation supporting defendant’s arguments of limited expression
1986	Bogle & Gates, Seattle	Breach of contract related to purchase and lease-back of software	Software - source code license for an operating system	Meaning of the term “version” in software development; performed inspection of source code for two versions of the operating system; deposition and testimony at trial
1978	Cesari & McKenna, Boston	Patent infringement	Computer hardware – I/O channel	Bus and input/output channel for hard disks, tapes and other peripherals; deposition and testimony at trial as inventor